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**\*BIBDATASHEET\*****CONFIRMATION NO. 4643**

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SERIAL NUMBER 10/792,308	FILING DATE 03/03/2004  RULE	CLASS 257	GROUP ART UNIT 2811	ATTORNEY DOCKET NO. 96-AG-114 (19321-DIV2)
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APPLICANTS

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**\*\* CONTINUING DATA \*\*\*\*\*** *yes a*

This application is a DIV of 10/158,706 05/30/2002 PAT 6,710,394  
 which is a DIV of 09/521,876 03/08/2000 PAT 6,399,444  
 which is a DIV of 08/940,856 09/29/1997 PAT 6,054,731

**\*\* FOREIGN APPLICATIONS \*\*\*\*\*** *yes a*

EUROPEAN PATENT OFFICE (EPO) 96830493.1 09/30/1996

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
**\*\* 05/24/2004**

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR  COUNTRY ITALY	SHEETS  DRAWING 4	TOTAL  CLAIMS 4	INDEPENDENT  CLAIMS 1
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials				

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TITLE  
 Method of making floating gate non-volatile memory cell with low erasing voltage having double layer gate dielectric

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees
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